Design of PID controller for automatic voltage regulator and validation using hardware in the loop technique

Deepak M. Sajnekar\textsuperscript{b}, Mohan L. Kolhe\textsuperscript{a*,} S.B. Deshpande\textsuperscript{c}, R. M. Moharil\textsuperscript{b}, Narayan P. Patidar\textsuperscript{d}, Koki Ogura\textsuperscript{e},

Abstract

The digital excitation control system (DECS) used for brushless synchronous generators has recently become popular owing to its ease of operation and flexibility to manage the operating parameters to achieve good maintenance of the output terminal voltage at the rated value. The latest industrial digital automatic voltage regulator (AVR) is used in the forward path of DECS. This paper addresses the issues of the DECS that are responsible for deviating the terminal voltage from the rated value and consequently making the power system unstable. A solution to the issue is provided using a new method of tuning the PID controller that eases the impact of discretization on the terminal voltage and keeps the DECS compatible with industrial standards. The controller designed in this way will be useful to maintain the terminal voltage of the synchronous generator to enhance the stability of the power system to which many renewable energy sources are interconnected. The proposed method improves the performance of the excitation control system by reducing the overshoot from 75% to 16%. The simulation results are presented using an industrial standard model of the excitation control system to demonstrate and compare the performance of the existing with the new digital excitation system. The newly designed controller was tested and validated using the “hardware in the loop” (HITL) technique.

Keywords: Automatic Voltage Regulator, PID Controller, Excitation Control System, Brushless Synchronous generator

1. Introduction

Synchronous generators excitation control systems are designed with controllers that allow a lot of setting flexibility to the operator to achieve an optimum generator terminal voltage response. Such controllers are still the choice of the industry owing to their high reliability, fast responses, simplicity and robustness [1]. However, owing to the advancements in “embedded system technology,” the trend of upgrading the existing to a digital excitation control system (DECS) is in progress [2] that offers an easy setting flexibility to the operator. A comparison with the popular existing PID controller tuning method [3] explains the advantages and disadvantages of each method and implements the digital PID controller using the pole zero cancellation method.

The role of ECS in the optimum performance of the generator is very important in terms of maintaining the parameters of the controller at an adequate level, which ensures the dynamic behavior in the generator terminal voltage, reactive power, and the power factor to keep the system stable. The guidelines to the designer in regard the range of the performance indexes such as rise time, overshoot, settling time, gain margin, phase margin, and bandwidth of the controller used for the excitation control
system are provided in the documents [4-7].

A non-linear controller has been developed for the excitation system to ensure adequate dynamic behavior [8]. However, industries are using a linear controller for the excitation control system. They prefer control strategies that do not require the complete data of the plant. This is intended to avoid time-consuming tests on the generator to evaluate the necessary parameters that are required to calculate the PID controller gains.

Currently, the developers of the control strategy use a complex model for the excitation control system that excludes the use of a standard model. The approach to use an intelligent control system provides a method to solve the stability problem of the system but is difficult to establish as a model using traditional equations. For example, the excitation control system is established using a neural network that is trained by the back-stepping algorithm, which improves the dynamic response in comparison to the conventional PID controller [9, 10]. Moreover, precise data for the system is required to design a neural network to avoid a dangerous transient response at the initial point.

Combinations of the intelligent control algorithm, conventional PID and fractional order PID controller have been proposed [11], which uses the intelligent algorithm to tune the PID parameters. With the help of precise data for the system in all these cases, optimum performance is observed. However, in the absence of precise data, the initial requirements such as initial weight for the neural network, trails in ant colony algorithms [12], norms in multi-objective optimization, population size in swarm optimization [13] and rules in the fuzzy technique are not precisely set; therefore, the control process fails. To calculate the analog controller parameters for the excitation systems and to operate it within the protection limits, a method was proposed in [14] to make the performance indexes of the designed controller to comply with the IEEE standard but the method is only suitable for analog controllers.

The PID controller’s self-tuning approach in modern excitation control system is used in the forward path. For example, the DECS reported in [15] uses the conventional method for PID gain calculation. Such a design results in an optimum performance even in the absence of precise data for the system. However, in this method the saturation data of the generator is used to calculate the PID gains and the sampling time of the processor needed to implement the digital PID controller is very small; this increases the labor and cost of the hardware. Feed forward control strategies have been evolved for further improvement of the performance in wind power applications [16], which show improved performance to achieve the rated speed when the wind speed is above the rated value. In some cases, a cascaded PD-PI and conventional PID controller have been considered for analyzing the dynamic response of the two-area interconnected power system in which to achieve enhanced performance controllers that are efficiently tuned by the “teaching learning based optimization” technique and improved performance has been reported [17]. The solution to the multi-machine low frequency inter-area oscillation is given by presenting an algorithm based wide area power system stabilizer [18], an unwanted effect caused due to sampling of error signal on the performance of the PID controller is in [19]. To minimize the uncertainties in the stability of the improved power system, a wide area fuzzy-2 logic-based damping controller was synthesized [20].

This paper proposes a method of tuning the PID controller used for the DECS of a synchronous generator that is an extension of the well-established PID tuning method used in industry for the existing excitation control system, known as “cancelling zeros by poles.” This method is modified to remove the unwanted effect of discretization on the controller performance by considering a suitable sampling time. The results show that when a discretized PID controller is tuned using the proposed method, it works well with a continuous plant and complies with the IEEE standard performance indexes. This design strategy also relieves the engineers and operators from the laborious task of calculating the PID gains while commissioning the new digital excitation system in the existing analog plant for upgrading.

The proposed method was tested on the IEEE standard simulation models to demonstrate the controller compatibility with the IEEE standard limit and was duly validated using Dspace 1104 industrial controller board hardware in the loop (HITL) that contains a continuous model of exciter and a brushless synchronous generator.
The rest of the paper is organized as follows: Section 2 explains the IEEE standard excitation control system model. Section 3 explains the methods used for the PID controller tuning with the aim to comply with the IEEE standard performance indexes. Section 4 presents the application example with the simulation results and discussion. Section 5 explains the experimental work used to validate the proposed method and finally, the paper is concluded in Section 6.

2. IEEE standard excitation control system model

A “Type AC 5A” excitation control system model was used for the study and is shown in Fig 1. It is a simplified model for brushless excitation systems with a rotating rectifier.

The voltage regulator of this system is supplied from a source such as a permanent magnet generator, which benefits the regulator by not affecting its source voltage in case of disturbance. Unlike the other ac models, this model uses loaded rather than open circuit exciter saturation data in the same manner as that used for the dc models. This is a simplified model that is widely used in the industry because detailed data for the system is not required for such implementation.

Fig. 1. The IEEE Type AC 5A model.

The model shown in Fig. 1 was modified to fit the PID controller and is shown in Fig 2. The design methodology proposed in this paper includes the calculation of the PID gains, i.e., the proportional gain ($K_P$), integral gain ($K_I$) and derivative gain ($K_D$) using the transfer function of the standard model to facilitate this. The model in Fig. 2 is reduced to Fig. 3.

Fig. 2. The IEEE Type AC 5A model with a PID controller.

In the reduced model, the generators terminal voltage is the output, the generator and exciter blocks are reduced to the first order model. Kg is the forward gain of the generator, and $t_g$ and $t_e$ are the generator and exciter time constants, respectively. The controller used to control the terminal voltage of the generator is a PID controller [2-3].
The open-loop transfer functions of the generator, exciter, and the PID controller are \( G_g(s) \), \( G_e(s) \) and \( G_c(s) \), respectively and their relationship is given in (1), (2), and (3). The closed loop transfer function “TEC(s)” of the reduced model shown in Fig. 3 is given in (4).

\[
G_g(s) = \frac{K_G}{t_g s + 1} \tag{1}
\]

\[
G_e(s) = \frac{1}{t_e s + 1} \tag{2}
\]

\[
G_c(s) = K_p + \frac{K_I}{s} + sK_D \tag{3}
\]

\[
T_{EC}(s) = \frac{G_c(s) \cdot G_g(s) \cdot G_e(s)}{1 + G_c(s) \cdot G_g(s) \cdot G_e(s)} \tag{4}
\]

The PID controller \( G_c(s) \) needs to be tuned using the appropriate values of the gains, i.e., \( K_P \), \( K_I \), and \( K_D \), in a manner such that the controller complies with the IEEE standard performance indexes.

To ensure the design of the PID controller used for the excitation control system will work in the bounded region to maintain the dynamic stability using the reduced model, the bode response of the IEEE standard and reduced model of a typical excitation control system is given in Fig. 4 as a reference to fix the scale of the design in terms of the phase margin because a reduced model gives an increased phase margin when compared to the IEEE standard model.
3. PID controller design aim to comply IEEE standard.

In the IEEE documents [4-7], guidelines are given in regard the identification, testing and evaluation of the dynamic performance of the excitation control system and accordingly, the accepted values of the performance indexes characterizing a good feedback control system performance are given in Table 1. The values reported in this table are for those generators that are not connected to the loads.

Table 1. The IEEE standard performance index.

<table>
<thead>
<tr>
<th>Index</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Overshoot</td>
<td>≤ 20%</td>
</tr>
<tr>
<td>Gain Margin</td>
<td>≥ 10 db</td>
</tr>
<tr>
<td>Phase Margin</td>
<td>≥ 40°</td>
</tr>
</tbody>
</table>

The range of the performance indexes of the excitation control system with small signal dynamics are given in Table 2. The values reported in this table vary in the specified range on the basis of the capacity of the generators and their loading conditions.

Table 2. The IEEE standard performance index with a small signal dynamic.

<table>
<thead>
<tr>
<th>Index</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Rise time</td>
<td>≤ 1 s</td>
</tr>
<tr>
<td>Settling time</td>
<td>≤ 4 s</td>
</tr>
<tr>
<td>Bandwidth</td>
<td>0.3 to 12 Hz</td>
</tr>
</tbody>
</table>

3.1 Analog PID controller design

The method selected for the analog PID controller design is “cancelling zeros by poles.” Academically, this method is impractical, but in the rotary excitation system, the poles corresponding to the generator and the exciter lie far away from each other (i.e., exciter poles lying 6 to 10 times away from the generator pole) in the pole zero map as reported in [3]. When the loop gain increases, the pole moves toward the corresponding zeros. Since the poles of the exciter and the generator are well separated, the resulting system through the cancellation of zeros by the poles does not affect the original systems.
required dynamics. This makes the selected method suitable for the rotary excitation system application [3].

The open-loop system transfer function of the reduced model is shown in Fig. 3 with a typical assumption of \( K_g = 1 \) and given in (5).

\[
G_e(s)G(s) = \frac{K_D}{t_g t_e^3} \left( \frac{s^2 + K_p}{K_D s + K_I} \right) \left( s + \frac{1}{t_g} \right) \left( s + \frac{1}{t_e} \right)
\]

(5)

The numerator and the denominator of the transfer function (5) have two zeros and three poles, and the cancellation of two zeros with two farthest poles of (5) is achieved by ensuring the equation in (6).

\[
\frac{s^2 + K_p}{K_D s + K_I} = 1 \Rightarrow \left( s + \frac{1}{t_g} \right) \left( s + \frac{1}{t_e} \right)
\]

(6)

Equating the numerator and denominator of (6) results in (7).

\[
s^2 + \frac{K_p}{K_D} s + \frac{K_I}{K_D} = \left( s + \frac{1}{t_g} \right) \left( s + \frac{1}{t_e} \right)
\]

(7)

Expansion and arrangement makes the RHS polynomial of (7) as in (8).

\[
s^2 + \frac{K_p}{K_D} s + \frac{K_I}{K_D} = s^2 + \left( \frac{t_g + t_e}{t_g t_e} \right) s + \frac{1}{t_g t_e}
\]

(8)

Equating the LHS and RHS of equation (8) gives an expression for \( K_P \) in (9) and \( K_I \) in (10).

\[
K_P = K_D \left( \frac{t_g + t_e}{t_g t_e} \right)
\]

(9)

\[
K_I = \frac{K_D}{t_g t_e}
\]

(10)

Submitting \( K_P \) and \( K_I \) in (5) results in (11).

\[
G_e(s)G(s) = \frac{K_D}{t_g t_e s}
\]

(11)

The closed loop transfer function of the system in Fig. 3 using (11) as in (12).

\[
\frac{G_c(s)G(s)}{1 + G_c(s)G(s)} = \frac{K_D}{t_g t_e s}
\]

(12)

The time response of the system in (12) to a unit step input is given in (13).
\[ C(t) = 1 - e^{-\frac{K_D}{t}t} \]  

(13)

If “\(t_r\)” is the required rise time for the design, it is defined as the time required for the response to rise from 10% to 90% of its final value and the resulting expression for \(K_D\) is given by (14).

\[ K_D = \frac{t_g t_e}{t_f K_G} \ln 9 \]  

(14)

Derivative gain \(K_D\) in (14) depends on the plant parameters \(tg, te,\) and the desired rise time “\(t_r.\)” An observation shows that the controller design is aims for a faster response with the proposed method making the settling time (\(ts\)), percentage peak overshoot (\(\% MP\)), gain margin (G.M) and phase margin (P.M) comply with the IEEE standard range, as shown in Table 1 and Table 2.

### 3.2 The design of the digital PID controller

The digital PID controller is implemented on a computer chip by the discretization of an integral and derivative block, and the computer chip reads the continuous error signal at regular time intervals, which is referred to as the sampling time.

In the proposed tuning method used for the discrete PID controller, the analog gains calculated earlier in subsection 3.1 were used with an assumption that the behavior of the discrete PID controller is similar to the continuous PID controller when the sampling time is very small.

Fig. 5. A comparison between the typical analog and the digital ECS’s step response. The discrete controller was tuned using a non-modified analog gain.

\[ G_c(z) = K_p + \frac{T(1+z^{-1})}{2.(1-z^{-1})} K_I + \frac{(1-z^{-1})}{T} K_D \]  

(15)

However, if the gains calculated using the existing method described earlier are directly used to implement the discrete PID controller, the system becomes unstable as described by the step and bode response of a typical excitation control system in Figs. 5 and 6.

The proposed method aims to tune the discrete PID controller with a large sampling time with a limit of being two times shorter than the lowest exciter time constant such that the design complies with the IEEE standard.
The existing PID controller in (3) is discretized by replacing the integral term “1/s” with “T(1 + z^{-1})/2(1 - z^{-1})” using trapezoidal integration and a derivative term “s” with (1 - z^{-1})/T, where T is the sampling time. Thus, the discretized controller equation is shown in (15).

Further arrangement of (16) results in (17).

Equating the RHS of (17) and (18) gives discrete PID gains K_{PD}, K_{ID} and K_{DD} in (19) in terms of...
The block diagram in Fig. 7 approximates the DECS where the discrete PID controller block interacts with a continuous exciter and a generator block to a continuous model. Therefore, the analysis of this system was performed by linearizing the discrete controller block. The analog PID gains $K_P$, $K_I$, and $K_D$ were calculated using equations (9), (10), and (14), and the resulting analog PID gains were used in equation (19) to calculate the discrete PID gains $K_{PD}$, $K_{ID}$, and $K_{DD}$. It is observed that with the adjustment of the forward loop gain, the “$K_G$ compatibility of the controller with the IEEE standard and, consequently, the stability of the system in Fig. 7 is easily ensured.

![Fig. 7. The approximated block diagram of a digital ECS.](image)

**4. Results and Discussion**

The rotary excitation system of 500 KVA, 440 V, and a 50 Hz brushless synchronous generator, whose manufacturer’s data are reported in Table 3, was used to demonstrate the methods described in Section 3.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>Generator time constant “tg”</td>
<td>2.2 s</td>
</tr>
<tr>
<td>Moment of Inertia</td>
<td>8.0068 Kg.m$^2$</td>
</tr>
<tr>
<td>Damping factor</td>
<td>65</td>
</tr>
<tr>
<td>No of poles</td>
<td>2</td>
</tr>
<tr>
<td>$R$</td>
<td>0.0049 Ω</td>
</tr>
<tr>
<td>$L$</td>
<td>0.1273 x 10$^{-3}$ H</td>
</tr>
</tbody>
</table>

![Table 3. The parameters of the brushless synchronous generator used in this study.](image)

The solution for this problem is the use of a PI controller or PID controller. The disadvantage of the PI controller is the integral gain that decreases the system’s dynamics and increases the phase delay and consequently decreases the system’s stability. The proposed controller used for this system is the PID controller whose derivative element compensates the system’s deceleration caused by an integral element.

The excitation system is an analog type and provided with an automatic volatage regulator (AVR) that
has a proportional “P” controller in the forward path. This generator’s output terminal voltage (PU) response for a 10% step increment and a 10% step decrement of the reference voltage is given in Fig. 8 and confirms the terminal voltage does not reach a steady state and consequently makes the power system unstable.

The data sheet provided by the manufacturer does not contain the value of the exciter time constant; therefore, for the design of the controller, the exciter time constant was approximated as one sixth of the generator time constant, as proposed in [3].

4.1 Example: Analog PID controller

The calculation of the PID gain for the controller to be used in the excitation system of the generator in Section 4 uses equation (9), (10), and (14) with a desired rise time (tr) of 0.236 s. The calculated PID gains are KP = 23, KI = 9, and KD = 7. Thus, the closed loop transfer function of the system in (4) is given in (20).

$$T_{EC}(s) = \frac{(7s^2 + 23s + 9)}{0.8067s^3 + 9.567s^2 + 24s + 9} \quad (20)$$

The step and bode response of the reduced system model in (20) was compared with the IEEE standard model (Fig. 2) and is shown in Figs. 9 and 10, respectively.

Fig. 9. A comparison of the reduced and IEEE standard model’s step response for the analog PID controller.

Fig. 10. A comparison of the reduced and IEEE standard model’s bode response for the analog PID controller.
It can be seen that the method ensures the compatibility of an analog PID controller with the IEEE standard performance indexes, as reported in Tables 1 and 2. The performance of the controller was further investigated by connecting it to a loaded generator and the load connected to the generator was capacitive.

The generator terminal voltage output response (PU) for a 10% step increment and 10% step decrement to the reference voltage is shown in Fig. 11, and the response for a 100% load connection at $t = 12$ s and rejection at $t = 13$ s is shown in Fig. 12, which confirms the stable and an improved operation of the generator with the controller.

**4.2 Example: Digital PID controller**

The analog PID controller gains described in the previous section were used to calculate the gains of the digital PID controller $K_{PP}$, $K_{ID}$, and $K_{DD}$ using equation (19) described in subsection B of Section 3; a sampling time of $T = 0.15$ s was used, which is two times shorter than the lowest exciter time constant. The analog gains $K_P$, $K_I$, and $K_D$ were multiplied by 10 to account for the other gain in the regulator. The digital PID controller gains calculated are $K_{PP} = 223$, $K_{ID} = 13$, and $K_{DD} = 466$.

Then, the design was tested on the simulation model of the standard IEEE excitation system 421.5 Type AC-5A as shown in Fig. 3 by considering the anti-reset and wind-up for integral action.
Fig. 13. The unit step response of the digital excitation control system (DECS).

Fig. 14. The Bode response of the DECS.

Fig. 15. The 10% step increment and decrement voltage response of the generator with a capacitive load.

Fig. 16. Load rejection: 100% Transient for online to offline transition.
The step and bode response of the designed controller used for the DECS after adjusting the value of
the forward gain KG to 0.045 is shown in Fig. 13 and 14, which show a rise time of 0.6 s, overshoot of
15.8%, settling time of 3.45 s, G.M of 18 db and P.M of 102°. The values of the performance indexes
comply with the IEEE standard and thus, the stability of the system is ensured.

The performance of the digital PID controller was further investigated by connecting it to a generator
with a capacitive load. The generator terminal voltage output response (PU) for a 10% step increment and
10% step decrement to the reference voltage is given in Fig. 15 and the response when a 100% load was
rejected at \( t = 20 \) s and reconnected at \( t = 22 \) s is shown in Fig. 16.

5. Experimentation Using DS 1104 Hardware

The DS1104 controller board is real-time hardware based on PowerPC technology, and its set of I/O
interfaces allows the controller board to be used for developing controllers for various systems with a
real-time interface (RTI). The function models on the DS1104 controller board can be easily configured
and run by inserting the blocks into a simulink block diagram and generating the model code via a
simulink coder.

The real-time model is compiled, downloaded, and started automatically in the MATLAB platform.
The experimental setup for the HITL experiment used to test the designed digital PID controller in the
excitation control system is given in Fig. 17. The discretized PID controller was built in a Dspace 1104
controller board using a RTI with the MATLAB simulink and the continuous model of the plant (i.e., the
exciter and the generator) was built in the PC.

Thus, the scheme takes the shape of hardware in the loop (HITL) and follows the IEEE standard model
given in Fig. 2. When the digital PID controller is used in the excitation system of 500 KVA, 440 V, and
a 50 Hz synchronous generator, the time response of this generator is shown in Fig. 18 wherein, the
terminal voltage reaches a steady state at \( t = 8 \) s and the stability of the system is ensured.

This experiment validates the method used for the design of the DECS. A comparison of the
performance of the PID controller with gains calculated using the proposed modified method and the non-
modified gains calculated using the cancelling poles by zeros method demonstrates the improvement in
the performance of the controller with the modified method, as reported in Table 4.
A PID controller has been designed using the proposed method to ensure the terminal voltage of a synchronous generator at the rated value. The use of the existing PID gains in the proposed modified method makes the design process not only simple but also ensures that the design of the discrete PID controller is IEEE compliant. The designed PID controller used in the AVR of a digital excitation system was tested and validated using the HITL technique. An investigation shows that the sampling time for the design may not necessarily be very small; thus, expensive processors can be avoided. However, the drawback of this method is that the customized and a competitive design of the digital PID controller cannot be ensured for the excitation system owing to the complexity involved in the design. This complexity is a result of the conversion of the discrete PID controller, sample and hold block into an s domain using the Pade approximation, which makes the order of the closed loop transfer function very high. This work can further be extended for a competitive and customized design of the digital PID controller with the aim of automating the tuning process of the PID controller in the digital excitation system.

References


